

FIG. 1

100

PROCESSOR 102

SELECTION LINES

SD ALARM 114

SF ALARM 116

122

120

ALARM CIRCUIT 112

CLOCK 118

ERRORS 108

RECEIVED MESSAGES 106

109

111

ENCODER 110

DECODER 104

[illegible]



Replacement
Sheet

Fig. 2b OTuk ROW (SHOWN AFTER INTERLEAVED TRANSMISSION)

COLUMN															
1	...	16	17	3824	3825	4080					
ROW		OH		PAYLOAD								FEC PARITY			

Fig. 2c

COLUMN																		
		1	...			14	15	16	17								3824	
ROW	1	OTuk OVERHEAD					OVERHEAD					OPUK PAYLOAD (4x 3808 bytes)						
	2	ODuk OVERHEAD					OVERHEAD											
	3	ODuk OVERHEAD					OVERHEAD											
	4	ODuk OVERHEAD					OVERHEAD											

ROW #	COLUMN #															
	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16
	FRAME ALIGNMENT BYTES				TCM ACT				TCM				RES			
	RES	TCM3	TCM6	TCM2	TCM1	TCM5	TCM4	PM	EXP	FTFL	OVERHEAD	OVERHEAD	OVERHEAD	OVERHEAD	OVERHEAD	OVERHEAD
1	GCC1				GCC2				APS/PCP				RES			
2	GCC1				GCC2				APS/PCP				RES			
3	GCC1				GCC2				APS/PCP				RES			
4	GCC1				GCC2				APS/PCP				RES			

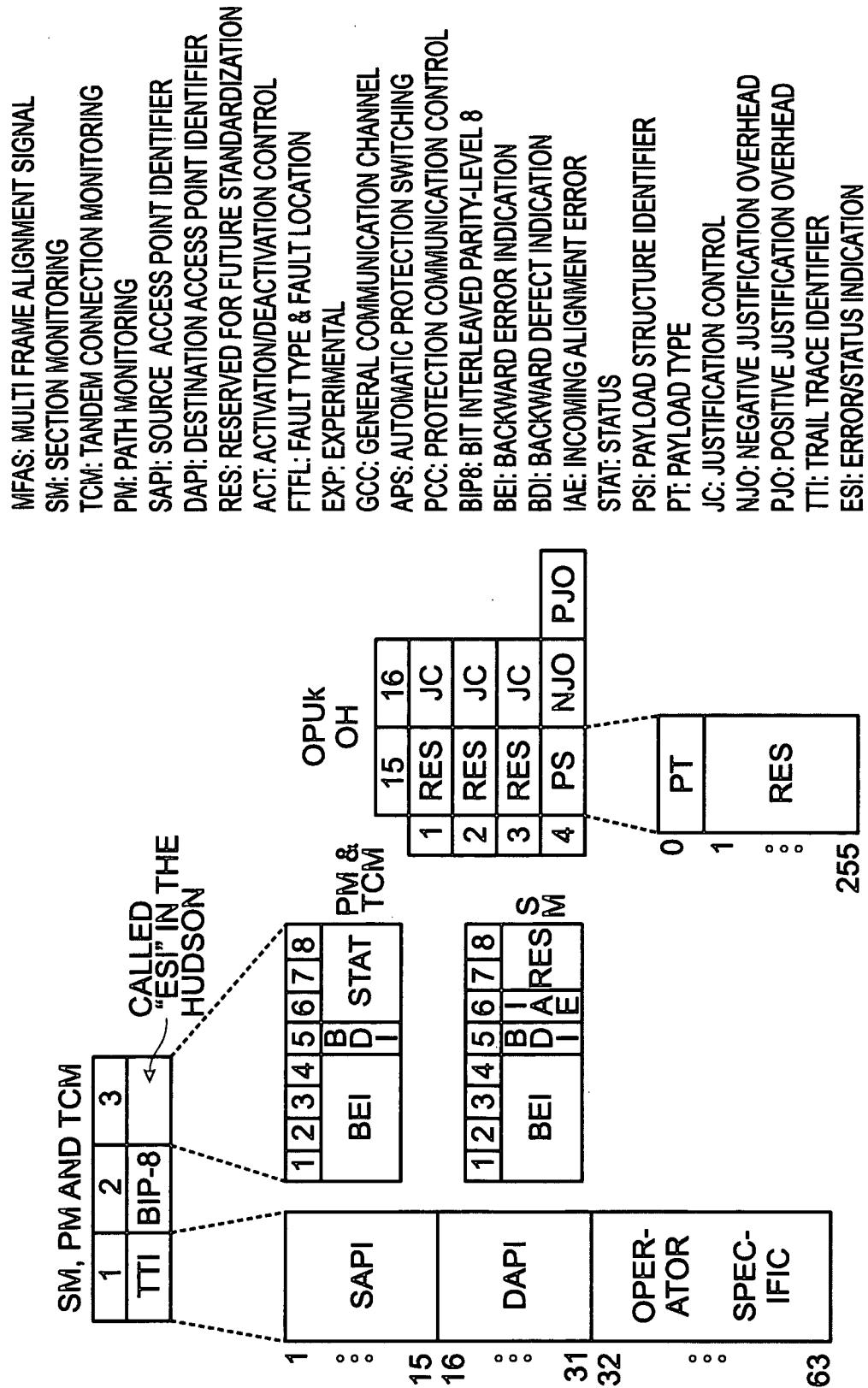




Fig. 3

